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AUG 21 2002

TECHNOLOGY CENTER 2800

Applicants : Hiroyuki Yano et al.
U.S. Serial No. : 09/870,085
Filing Date : May 30, 2001
For : **MANUFACTURING METHOD OF SEMICONDUCTOR
DEVICE**
Examiner : Thanhha Pham
Group Art Unit : 2813

#17
IDS
J. M. C. Sullivan
8/22/02

745 Fifth Avenue
New York, New York 10151

I hereby certify that this correspondence is being deposited with
the United States Postal Service as first class mail in an envelope
addressed to: Assistant Commissioner for Patents, Washington,
DC 20231, on .

Grace L. Pan, Reg. No. 39,440

Name of Applicant, Assignee or Registered Representative

Grace L. Pan

Signature

July 30, 2002

Date of Signature

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

The Examiner's attention is respectfully invited to review the following enclosed
and listed document which is also listed on the accompanying Form PTO-1449, enclosed in
duplicate.

08/23/2002 JMCNILLA 00000000 500320 09870085

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FOREIGN PATENT

Japanese Unexamined Patent Publication Tokukai Sho. 62-154614

Published: July 9, 1987 to Imaoka *et al.*

This Information Disclosure Statement is not a representation that the document cited herein is considered most pertinent, or that a search has been undertaken, or that any of the cited document is indeed prior art. The Examiner is invited to undertake an independent search. Applicants assert that the claimed invention is patentable over this document.


Applicants respectfully request that the Examiner consider and make of record the document cited herein and that a copy of Form PTO-1449, appropriately initialed by the Examiner, be returned to Applicants' attorney.

It is believed no fee is due, however, the Examiner is authorized to charge any deficit or credit any overpayment to Deposit Account No. 50-0320.

Respectfully submitted,

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By:



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